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Substitute for form 1449/PTO			, .	Complete if Known			
				Application Number	10/760,652		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use as many sheets as necessary)				Filing Date	01/20/2004		
				First Named Inventor	Gregory Edward Tierney		
				Art Unit	2186		
				Examiner Name	Chery, Mardochee		
Sheet	1	of	1	Attorney Docket Number	200313614-1		

		NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
		MARTIN, MILO M.K., ET AL., "Token Coherence: Decoupling Performance And Correctness", ISCH-30, pages 1-12, June 9-11, 2003		
		RAJEEV, JOSHI ET AL., " Checking Cache-Coherence Protocols With TLA+", Kluwer Adacemic Publishers, 2003, pages 1-8		
		HANDY, "The Cache Memory Book", 1998, Academic Press second edition, pages 144-155		
		CHEN ET AL., "Write Caching in Distributed Filed Systems", Distributed Computing Systems, 1995., Proceedings Of The 15th Int'l Con., 30 May-2 June 1995, Pages 457-466		
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		GHARACHORLOO, KOUROSH ET AL., "Architecture and Design of AlphaServer GS320", Western Research Laboratory, (Date Unknown)		
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		MARTIN ET AL., "Bandwidth Adaptive Snooping", Proceedings Of The Eighth International Symposium On High-Performance Computer Architecture (HPCA'02), 2002 IEEE		
		ACACIO, MANUEL ET AL., "Owner Prediction For Accelerating Cache-to-Cache Transfer Misses In A cc-NUMA Architecture", IEEE 2002		

Examiner	Date	
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<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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